## U.S. Patent Application Serial No. 09/973,002

## IN THE CLAIMS:

Proposed Claim Amendment:

- 1. (Twice Amended) A semiconductor device comprising:
  - a substrate having a first plurality of pads and a second plurality of pads;
- a first semiconductor chip mounted on said substrate and having a third plurality of pads;
- a plate member arranged on said first semiconductor chip and having an end at an inward position of said first semiconductor chip adjacent to the third plurality of pads;
- a second semiconductor chip arranged on said plate member and having a fourth plurality of pads;
- a first structure and a second structure respectively and electrically interconnecting said third plurality of pads with said first plurality of pads and said fourth plurality of pads with said second plurality of pads; and
- a seal member sealing said first semiconductor chip, said plate member and said second semiconductor chip and having a seal member side surface;

wherein said plate member has a first portion covered by said first and second semiconductor chips, and a second portion protruding from said first and second semiconductor chips and extending through said seal member to the seal member side surface whereby said plate member is exposed to an outside of said seal member.

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2. (Amended) A semiconductor device according to claim 1, wherein said first structure

and said second structure are made of bonding wires.

5. (Twice Amended) A semiconductor device according to claim 1, wherein said plate

member includes a fifth plurality of pads, part of said second structure electrically inter-connecting

part of said fourth plurality of pads with part of said second plurality of pads via said fifth plurality

of pads.

Cancel claim 6 without prejudice.